



20 October 2023

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**Council for Trade-Related Aspects of
Intellectual Property Rights**

Original: English

**NOTIFICATION OF LAWS AND REGULATIONS
UNDER ARTICLE 63.2 OF THE TRIPS AGREEMENT**

TÜRKIYE: REGULATION ON THE IMPLEMENTATION OF LAW NO. 5147 ON THE
PROTECTION OF LAYOUT-DESIGNS OF INTEGRATED CIRCUITS

Notifying Member	TÜRKIYE
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Details of the notified legal text

Title	Regulation on the Implementation of Law No. 5147 on the Protection of Layout-Designs of Integrated Circuits
Subject matter	Layout-designs (topographies) of integrated circuits
Nature of notification	<input checked="" type="checkbox"/> Main dedicated intellectual property law or regulation <input type="checkbox"/> Other law or regulation
Link to legal text*	https://ip-documents.info/2023/IP/TUR/23_11351_00_x.pdf
Notification status	<input checked="" type="checkbox"/> First notification <input type="checkbox"/> Amendment or revision to notified legal text <input type="checkbox"/> Replacement or consolidation of notified legal text(s)
Previous notification(s) referred to	Not applicable
Brief description of the notified legal text	
This Regulation covers the place and time of the integrated circuit topography registration application, the preparation and registration of the application documents and the procedures and principles to be followed in other transactions.	
Language(s) of notified legal text	English
Entry into force	30 December 2004; Amendments: 18 January 2015, 27 November 2015, 23 February 2018, 8 July 2019, 14 April 2021
Other date	

Notification details

Submission date of notification	28 March 2023
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Other information	See also: IP/N/1/TUR/L/2 <i>(Law No. 5147 on the Protection of Layout-Designs of Integrated Circuits)</i> https://www.mevzuat.gov.tr/mevzuat?MevzuatNo=7220&MevzuatTur=7&MevzuatTertip=5 <i>(Regulation on the Implementation of Law No. 5147 on the Protection of Layout-Designs of Integrated Circuits (Turkish))</i> https://www.mevzuat.gov.tr/mevzuat?MevzuatNo=5147&MevzuatTur=1&MevzuatTertip=5 <i>(Law No. 5147 on the Protection of Layout-Designs of Integrated Circuits (Turkish))</i>
Agency or authority responsible	Turkish Patent and Trademark Office (TURKPATENT) 13 Hipodrom Street 06560 Yenimahalle Ankara Türkiye https://www.turkpatent.gov.tr Telephone: (+90 312) 303 1 303 E-Mail: contact@turkpatent.gov.tr

* Links are provided to texts of laws and regulations notified under the TRIPS Agreement in the form supplied by the Member concerned; the WTO Secretariat does not endorse or revise their content.