

29 July 2022

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(22-5733)

Original: English

Council for Trade-Related Aspects of Intellectual Property Rights

NOTIFICATION OF LAWS AND REGULATIONS UNDER ARTICLE 63.2 OF THE TRIPS AGREEMENT

TONGA: PROTECTION OF LAYOUT DESIGNS (TOPOGRAPHIES) OF INTEGRATED CIRCUITS REGULATIONS - CHAPTER 17.13.01 (2020 REVISED EDITION)

Notifying Member Tonga

Details of the notified legal text

Title	Protection of Layout Designs (Topographies) of Integrated Circuits Regulations - Chapter 17.13.01 (2020 Revised Edition)
Subject matter	Layout-designs (topographies) of integrated circuits
Nature of notification	[X] Main dedicated intellectual property law or regulation[] Other law or regulation
Link to legal text*	https://ip-documents.info/2022/IP/TON/22_4409_00_e.pdf
Notification status	 [X] First notification [] Amendment or revision to notified legal text [] Replacement or consolidation of notified legal text(s)
Previous notification(s) referred to	Not applicable
Brief description of the notified legal text	
These Regulations are made under section 21 of the Protection of Layout-Designs (Topographies) of Integrated Circuits Act [Cap.17.13]. The Regulations provide for the payment of fees in connection with applications for the registration of layout-designs of integrated circuits and matters related thereto.	
Language(s) of	English

notified legal text	
Entry into force	1 September 2008
Other date	

Notification details

Submission date of notification	29 June 2022
Other information	See also document <u>IP/N/1/TON/L/1</u> (Protection of Layout-Designs (Topographies) of Integrated Circuits Act)

IP/N/1/TON/3 • IP/N/1/TON/L/2

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* Links are provided to texts of laws and regulations notified under the TRIPS Agreement in the form supplied by the Member concerned; the WTO Secretariat does not endorse or revise their content.

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