

## IP/N/1/TON/9 IP/N/1/TON/L/3

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Council for Trade-Related Aspects of Intellectual Property Rights

## NOTIFICATION OF LAWS AND REGULATIONS UNDER ARTICLE 63.2 OF THE TRIPS AGREEMENT

TONGA: PROTECTION OF LAYOUT-DESIGNS (TOPOGRAPHIES) OF INTEGRATED CIRCUITS ACT - CHAPTER 17.13 - 2020 REVISED EDITION

<b>Notifying Member</b>	Tonga

## Details of the notified legal text

Title	Protection of Layout-Designs (Topographies) of Integrated Circuits Act - Chapter 17.13 - 2020 Revised Edition	
Subject matter	Layout-designs (topographies) of integrated circuits	
Nature of notification	[X] Main dedicated intellectual property law or regulation [ ] Other law or regulation	
Link to legal text*	https://ip-documents.info/2022/IP/TON/22 4408 00 e.pdf	
Notification status	<ul><li>[ ] First notification</li><li>[ ] Amendment or revision to notified legal text</li><li>[X] Replacement or consolidation of notified legal text(s)</li></ul>	
Previous notification(s) referred to	IP/N/1/TON/L/1	
Brief description of the notified legal text		
An Act to make provision for the protection of layout-designs (topographies) of integrated circuits.		
Language(s) of notified legal text	English	
Entry into force	1 September 2008	
Other date		

## **Notification details**

Submission date of notification	29 June 2022
Other information	

Agency or authority responsible

Ms Distaquaine Tu'ihalamaka Chief Executive Officer

Ministry of Trade and Economic Development

P.O. Box 110 Nuku'alofa Tonga

Telephone: +(676) 7400105 Telefax: +(676) 23887

E-mail: <a href="mailto:ceo@mted.gov.to">ceo@mted.gov.to</a>
Website: <a href="mailto:www.mted.gov.to">www.mted.gov.to</a>

 $<sup>^{</sup>st}$  Links are provided to texts of laws and regulations notified under the TRIPS Agreement in the form supplied by the Member concerned; the WTO Secretariat does not endorse or revise their content.