## Decree No. 2001-1984 of August 27, 2001, concerning the amounts of the fees relating to layout-designs of integrated circuits

The President of the Republic,

On a proposal by the Minister of Industry,

Considering Law No. 82-66 of August 6, 1982, on Standardization and Quality, in particular Articles 4 and 17,

Considering Law No. 2001-20 of February 6, 2001, on the Protection of Layout-Designs of Integrated Circuits,

Considering Decree No. 82-1314 of September 24, 1982, concerning the Organization and Functioning of the National Institute of Standardization and Industrial Property, in particular Article 11,

Considering Decree No. 98-2133 of November 2, 1998, concerning the amounts of the fees relating to industrial property,

Considering the opinion of the Minister of Finance,

Considering the opinion of the administrative tribunal.

Hereby decrees:

1. The amounts of the fees relating to layout-designs of integrated circuits, as provided for in Articles 8, 13, 14, 15 and 20 of the above Law No. 2001-20 of February 6, 2001, shall be fixed in accordance with the table attached to this Decree.

The amounts of the fees shall not include V.A.T.

- **2.** The fees referred to in Article one of this Decree shall be collected in accordance with the same procedures as laid down in Table "D," attached to the above Decree No. 98-2133 of November 2, 1998.
- **3.** The Minister of Finance and the Minister of Industry shall, within their individual remits, be responsible for enforcing this Decree which shall be published in the Official Gazette of the Republic of Tunisia.

Tunis, August 27, 2001. Zin El Abidin Bin Ali

## **ANNEX**

## Amounts of Fees Relevant to Layout-Designs of Integrated Circuits

Nature of operation	Amount in dinars
Deposit of a layout-design of integrated circuits	Thirty (30)
Withdrawal of the deposit of a layout-design of integrated circuits	Seventy (70)
Entry of any act amending or transferring the rights attached to a deposited layout-design of integrated circuits	Thirty (30)
Entry of any amendment of the deposit of a layout-design of integrated circuits entered in the Register	Twenty (20)
Issue of a copy of an entry in the Register of Layout-Designs of Integrated Circuits	Thirty (30)
Issue of a copy of the file for the deposit of a layout-design of integrated circuits	Hundred (100)
Surrender of the deposit of a layout-design of integrated circuits	Thirty (30)

Note: Translation by the International Bureau of WIPO.