



20 October 2022

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**Council for Trade-Related Aspects of
Intellectual Property Rights**

Original: English

**NOTIFICATION OF LAWS AND REGULATIONS
UNDER ARTICLE 63.2 OF THE TRIPS AGREEMENT**

LITHUANIA: LAW ON THE LEGAL PROTECTION OF TOPOGRAPHIES
OF SEMICONDUCTOR PRODUCTS

Notifying Member	LITHUANIA
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Details of the notified legal text

Title	Law on the Legal Protection of Topographies of Semiconductor Products
Subject matter	Layout-designs (topographies) of integrated circuits
Nature of notification	<input checked="" type="checkbox"/> Main dedicated intellectual property law or regulation <input type="checkbox"/> Other law or regulation
Link to legal text*	https://ip-documents.info/2022/IP/LTU/22_7000_00_e.pdf
Notification status	<input type="checkbox"/> First notification <input type="checkbox"/> Amendment or revision to notified legal text <input checked="" type="checkbox"/> Replacement or consolidation of notified legal text(s)
Previous notification(s) referred to	IP/N/1/LTU/L/1
Brief description of the notified legal text	
This Law shall provide for the legal protection of topographies of semiconductor products and regulate the rights and duties of natural and legal persons arising after the creation and during the exploitation of topographies of semiconductor products.	
Language(s) of notified legal text	English
Entry into force	7 July 2017; As last amended on 30 March 2017
Other date	

Notification details

Submission date of notification	21 September 2022
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Other information	https://vpb.lrv.lt/uploads/vpb/documents/files/LAW%20ON%20THE%20LEGAL%20PROTECTION%20OF%20TOPOGRAPHIES%20OF%20SEMICONDUCTOR%20PRODUCTS.pdf
Agency or authority responsible	The State Patent Bureau of the Republic of Lithuania Kalvariju str. 3, LT-09310 Vilnius Lithuania Email: info@vpb.gov.lt

* Links are provided to texts of laws and regulations notified under the TRIPS Agreement in the form supplied by the Member concerned; the WTO Secretariat does not endorse or revise their content.