

L.N. 65 of 2013

**Layout-design (Topography) of Integrated Circuits
(Designation of Qualifying Countries, Territories or
Areas) (Amendment) Regulation 2013**

(Made by the Chief Executive under section 24 of the Layout-design (Topography) of Integrated Circuits Ordinance (Cap. 445) after consultation with the Executive Council)

1. Commencement

This Regulation comes into operation on 28 June 2013.

2. Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation amended

The Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation (Cap. 445 sub. leg. B) is amended as set out in section 3.

3. Schedule amended (qualifying countries, territories or areas)

(1) The Schedule, English text—

Repeal

“Negara Brunei Darussalam”

Substitute

“Brunei Darussalam”.

(2) The Schedule, after item “Republic of Estonia”—

Add

“European Union”.

(3) The Schedule—

Repeal

“The Republic of The Fiji Islands”

Substitute

“The Republic of Fiji”.

- (4) The Schedule, after item “Kyrgyz Republic”—

Add

“The Lao People’s Democratic Republic”.

- (5) The Schedule, after item “Mongolia”—

Add

“Montenegro”.

- (6) The Schedule—

Repeal

“The Union of Myanmar”

Substitute

“The Republic of the Union of Myanmar”.

- (7) The Schedule—

Repeal

“Netherlands—For the Kingdom in Europe and for the Netherlands Antilles”

Substitute

“The Kingdom of the Netherlands”.

- (8) The Schedule, after item “Romania”—

Add

“The Russian Federation”.

- (9) The Schedule, after item “Saint Vincent and the Grenadines”—

Add

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Section 3

B1909

“The Independent State of Samoa”.

(10) The Schedule, after item “Oriental Republic of Uruguay”—

Add

“The Republic of Vanuatu”.

C. Y. LEUNG
Chief Executive

25 April 2013

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Countries, Territories or Areas) (Amendment) Regulation 2013

Explanatory Note
Paragraph 1

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B1911

Explanatory Note

This Regulation updates the list of qualifying countries, territories or areas contained in the Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation (Cap. 445 sub. leg. B).